



*FR*

PATENT  
Docket No. Intel/17479

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicant(s): ) I hereby certify that this paper and the  
Kiran R. DESAI ) documents referred to as enclosed  
U.S. Serial No.: 10/630,164 ) therewith are being deposited with the  
For: "Methods and Apparatus for ) United States Postal Service as first class  
Maintaining Cache Coherency" ) mail, postage prepaid, in an envelope  
Filed: July 30, 2003 ) addressed to Commissioner for Patents,  
Assignee: Intel Corporation ) P.O. Box 1450, Alexandria, Virginia  
Group Art Unit: 2186 ) 22313-1450 on this date:  
Examiner: Not Yet Assigned )  
August 30, 2005  
*James A. Flight*  
James A. Flight  
Attorney for Applicant(s)  
Registration No. 37,622

**STATUS LETTER**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Kindly advise when an Office action can be expected in the above-referenced matter.

Respectfully submitted,

HANLEY, FLIGHT & ZIMMERMAN, LLC  
USPTO Customer Number 34431  
20 North Wacker Drive  
Suite 4220  
Chicago, Illinois 60606  
(312) 580-1020

August 30, 2005

By:

*James A. Flight*  
James A. Flight  
Registration No.: 37,622